

## REMARKS

Applicant respectfully requests reconsideration of the rejections in view of the foregoing amendments and following remarks.

### Claim Status

Examiner objected to claims 21 due to an informality. Examiner rejected claims 1–3, 7, 9–11, 15, and 17–21 under 35 U.S.C. § 102(e) as anticipated by Gorday (U.S. 2005/0074036). Applicant amends claim 21 to correct the informality. Claims 1–3, 7, 9–11, 15, and 17–21 remain pending.

### Claims 1–3, 7, 9–11, 15, and 17–21

Independent claim 1 recites, in part, “a first receiver path for decoding a preamble to a wireless data packet and a second receiver path for decoding a data packet payload.” Independent claims 10 and 18 recite similar limitations. However, the cited reference fails to teach or suggest this combination of limitations. On page 4 of the Office Action, Examiner cites the low power sleep mode of Gorday as allegedly teaching the first receiver path of the limitation and the active mode of Gorday as allegedly teaching the second receiver path of the limitation. Examiner also cites, inter alia, ¶ 0013 of Gorday. However, the low power sleep mode of Gorday fails to teach or suggest decoding a preamble to the wireless data packet of the limitation as required by the claims. Indeed, Gorday teaches the opposite. At ¶ 0013, Gorday states:

The receiver receives at least one of the frequency synchronization bursts. The receiver adjusts its frequency in order to match the transmission frequency of the data packets. **Thereafter**, the receiver may switch to a low power sleep mode until the beginning of the transmission of data packets. The receiver switches back to active mode **just before** the transmission of data packets from the transmitter. The receiver receives the data packets at the adjusted frequency of the receiver.

As can be seen, nothing is decoded in the low power sleep mode of Gorday because the low power sleep mode is entered into after receiving the frequency synchronization burst and exited before the transmission of data packets. As Examiner interprets the low power sleep mode to be the first receiver path of the limitation, Gorday fails teach or suggest a first receiver path for decoding a data packet payload as required by the claim. For the same reason, dependent claims 9 and 17 are allowable over Gorday: the low power sleep mode of Gorday is entered into after the frequency

synchronization burst is identified, not “until the data packet is identified” as required by the claims. For at least this reason, independent claims 1, 10, and 18, along with dependent claims 2–3, 5–7, 9, 11, 13–17, and 19–21, are allowable over Gorday.

**Conclusion**

For the reasons stated above, Applicant respectfully submits that the application is in condition for allowance. In the course of the foregoing discussions, Applicant may have at times referred to claim limitations in shorthand fashion, or may have focused on a particular claim element. This discussion should not be interpreted to mean that the other limitations can be ignored or dismissed, or that limitations from the specification can be imported into the claims. The claims must be viewed as a whole, and each limitation of the claims must be considered when determining the patentability of the claims. Moreover, it should be understood that there may be other distinctions between the claims and the prior art which have yet to be raised, but which may be raised in the future.

Applicant respectfully requests reconsideration and that a timely Notice of Allowance be issued in this case. It is believed that no extensions of time or fees are required, beyond those that may otherwise be provided for in attachments accompanying this document. However, in the event that additional extensions of time are necessary to allow consideration of this document, such extensions are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required (including fees for net addition of claims) are hereby authorized to be charged to Texas Instruments Incorporated Deposit Account Number 20-0668.

Respectfully submitted,

/Tim D. Chheda/

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